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MMK Martin, DJ Sorin, HW Cain, MD Hill, ... - Proceedings of the ... 2001 - portal.acm.org
... **multithreaded** processors can implement this similarly, by keeping a **per-thread** table of
speculatively loaded addresses and checking all the stores of the other threads ... Examples of these
relaxed models include SPARC TSO, **IA-32**, SPARC RMO, PowerPC, and **IA-64**. ...
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[PDF] Using PAPI for hardware performance monitoring on Linux systems   [PDF] from psu.edu
J Dongarra, K London, S Moore, P Mucci, D ... - Conference on Linux ..., 2001 - Citeseer
... The PAPI implementation for **IA-32** Linux uses the perfctr Linux patch to enable access to ... to the
counters under **IA-64** Linux, PAPI uses the latest **IA-64** Linux patch ... work involves extending the
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D Bruening, S Amarasinghe - Proceedings of the international ..., 2005 - portal.acm.org
... To support all applications on **IA-32** we must follow se- quential consistency [21], which ... on every
cache flush, versus our non-precise flushing, on our **multithreaded** benchmarks. ... although once
fragments are duplicated they can be special- ized **per thread**, facilitating thread ...
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Scalable lock-free dynamic memory allocation   [PDF] from psu.edu
MM Michael - Proceedings of the ACM SIGPLAN 2004 conference ..., 2004 - portal.acm.org
... 1. INTRODUCTION Dynamic memory allocation functions, such as malloc and free, are heavily
used by a wide range of important **multi- threaded** applications, from ... CAS was introduced on the
IBM System 370 [8]. It is supported on Intel (**IA-32** and **IA-64**) and Sun SPARC ar ...
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The open runtime platform: A flexible high-performance managed runtime environment   [PDF] from ku.edu
M Cierniak, M Eng, N Glew, B Lewis ... - Concurrency and ..., 2005 - interscience.wiley.com
... Windows or Linux operating systems, and to run on the **IA-32** or Itanium ... technologies in
just-in-time compilers (JITs), garbage collection (GC), **multithreading**, and synchronization. ... Third,
MRTEs are **multi-threaded**, providing facilities for the creation and management of threads ...
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Performance instrumentation and measurement for terascale systems   [PDF] from psu.edu
J Dongarra, A Malony, S Moore, P Mucci, S ... - Science—ICCS 2003, 2003 - Springer
... and requires a continuous porting effort as new computing platforms and **multi-threaded**
programming environments ... SGI IRIX, IBM AIX Power, Sun Ultrasparc Solaris, Linux/x86,
Linux/**IA- 64**, HP/Compaq ... Dynaprof uses Dyninst API [3] on Linux/**IA-32**, SGI IRIX, and Sun Solaris ...
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64-bit versus 32-bit Virtual Machines for Java   [PDF] from psu.edu
K Venstermans, L Eeckhout, K De ... - Software: Practice and ..., 2006 - Wiley Online Library
... to support a large variety of instruction set architectures (ISAs), such as **IA-32**, **IA-64**, PowerPC,
Alpha ... However, AMD recently announced 64-bit hardware extending the **IA-32** ISA in its ... RVM
was extended to support reading the Hardware Performance Monitors **per thread** [7]. We ...
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[PDF] Optimising Java Programs Through Basic Block Dynamic Compilation   [PDF] from man.ac.uk
IA Rogers - 2002 - cs.man.ac.uk
... **Multithreading** allows parts of a program (called threads) to execute at the same time. By having
threads designed into the Java programming language, programmers can write **multithreaded**
programs without having to rely on thread- support libraries. ...
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Java JNI bridge: A framework for mixed native ISA execution
M Chen, S Goldsmeberg, S Srinivas, V Ushakov, Y Wang ... 2006 - computer.org
... management, up- and down-calls, and source architecture execution environment features (signal
handling, and **multithreading**). ... A stub method in the **IA-64** binary redirects the call to the DBT.
When the DBT is done executing the **IA-32** method, its return value is obtained from ...
Cited by 1 - Related articles - All 5 versions

[PDF] Scalable Performance Analysis Infrastructure for Terascale Systems   [PDF] from utk.edu
J Dongarra, A Malony, S Moore, P Mucci, S ... - Future Generation ..., 2004 - citeulik.edu
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programming environments ... SGI IRIX, IBM AIX Power, Sun Ultrasparc Solaris, Linux/x86,
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